Field Effect Transistor

Silicon N Channel MOS Type (t-MOS IV)

High Speed, High Current Switching Applications

Features

- Low Drain-Source ON Resistance
- $R_{DS(ON)} = 0.24\Omega$ (Typ.) High Forward Transfer Admittance
- $|Y_{fs}|$ = 15S (Typ.)
- Low Leakage Current
 - $-I_{DSS} = -100\mu A \text{ (Max.) (V}_{DS} = 500\text{V)}$
- Enhancement-Mode
 - $V_{th} = 2.0 \sim 4.0 V (V_{DS} = -10 V, I_D = 1 mA)$

Absolute Maximum Ratings (Ta = 25C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Drain-Source Voltage		V _{DSS}	500	٧	
Drain-Gate Voltage (R _S = 20kΩ) Gate-Source Voltage		V _{DGR} Vass	500	٧	
			±30	٧	
Drain Current	DC	Ь	20	Α	
	Pulse	1 _{OP}	80		
Drain Power Dissipation (Tc = 25°C)		P _D	150	W	
Channel Temperature		Th	150	°C	
Storage Temperature Range		- Itg	-55 - 150	°C	

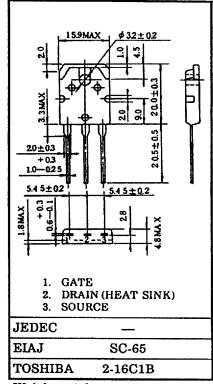
Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	ff(ch-c)	0.833	°C/W
Thermal Resistance, Channel to Ambient	fil(ch-a)	50	°C/W

This transister is an electrostatic sensitive device. Please handle with caution.

Industrial Applications

Unit in mm



Weight: 4.6g

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1/2

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Electrical Characteristics (Ta = 25C)

CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Cur	Leakage Current k_{SS} $V_{GS} = \pm 25V$, $V_{DS} = 0V$				±10	nA	
Gate-Source Breakdown Voltage		V _{(BR) DSS}	$I_{G} = \pm 100V, V_{DS} = 0V$	±30	-	- 1	μA
Drain Cut-off Cur	rent	loss	$V_{DS} = 500V$, $V_{GS} = 0V$	-	_	100	μA
Drain-Source Bre	akdown Voltage	(BR) DSS	I _D = 10mA, V _{GS} = 0V	500	_	-	٧
Gate Threshold Voltage		V h	$V_{DS} = 10V$, $b = -1mA$	2.0	-	4.0	٧
Drain-Source ON	Resistance	Pos (ON)	V _{GS} = 10V, I _D = 10A	-	0.24	0.30	Ω
Forward Transfer	Admittance	Y _{ts} I	V _{DS} = 10V, I _{DS} = 10A	10	15	-	S
Input Capacitance Reverse Transfer Capacitance Output Capacitance		Gss	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz	-	3000	4800	DF
		Çss			220	270	
		Coss			830	1200	μı
	Rise Time	tr		_	25	50	
Switching Time	Turn-on Time	bn	ID-IOA	-	60	120	ns
•	Fall Time	1	Ves er 10 a	_	55	110	113
Turn-	Turn-off Time	рщ	Ves of 100 Vest 100 Vest 100 Vest 100 Vest 100 Vest 200 V	_	280	560	
(Gate-Source Plus Gate-Drain)		Qg	Q _g V _{DD} = 400V, V _{GS} = -10V, ID = -20A	_	65	130	nC
		Q _s			40	_	
Gate-Drain ("Mill	er") Charge	Q _d		_	25	-	

Source-Drain Diode Ratings and Characteristics (Ta = 25C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	de	-		_	20	Α
Pulse Drain Reverse Current	DRP	-	- 1		80	Α
Diode Forward Voltage	Yosf	I _{DR} = 20A, V _{GS} = 0V		-1.0	-1.7	٧
Reverse Recovery Time	*	I _{DR} = 20A, V _{GS} = 0V		450	-	ns
Reverse Recovered Charge	Q,	dl _{DR} / _{dt} = 100A/µs		6.8	-	μC

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2/2