# BT151B series

#### GENERAL DESCRIPTION

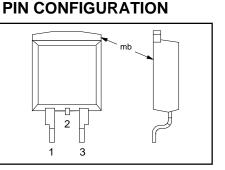
Glass passivated thyristors in a plastic envelope, suitable surface for mounting, intended for use in applications requiring high bidirectional voltage blocking capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

# QUICK REFERENCE DATA

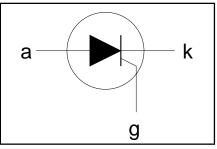
SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V <sub>DRM</sub> , Vppy	BT151B- Repetitive peak off-state voltages	<b>500R</b> 500	<b>650R</b> 650	<b>800R</b> 800	V
V <sub>RRM</sub> I <sub>T(AV)</sub> I <sub>T(RMS)</sub> I <sub>TSM</sub>	Average on-state current RMS on-state current Non-repetitive peak on-state current	7.5 12 100	7.5 12 100	7.5 12 100	A A A

#### PINNING - SOT404

# PINDESCRIPTION1cathode2anode3gatembanode



### SYMBOL



#### LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
$V_{drm}, V_{rrm}$	Repetitive peak off-state voltages		-	<b>-500R</b> 500 <sup>1</sup>	<b>-650R</b> 650 <sup>1</sup>	<b>-800R</b> 800	V
I <sub>T(AV)</sub> I <sub>T(RMS)</sub> I <sub>TSM</sub>	Average on-state current RMS on-state current Non-repetitive peak on-state current	half sine wave; $T_{mb} \le 109$ °C all conduction angles half sine wave; $T_j = 25$ °C prior to surge	-		7.5 12		A A
		t = 10 ms t = 8.3 ms	-		100 110		A A A²s
l²t dI <sub>⊤</sub> /dt	I <sup>2</sup> t for fusing Repetitive rate of rise of on-state current after triggering		-		50 50		A²s A∕µs
$\begin{matrix} I_{GM} \\ V_{GM} \\ V_{RGM} \\ P_{GM} \\ P_{G(AV)} \end{matrix}$	Peak gate current Peak gate voltage Peak reverse gate voltage Peak gate power Average gate power	over any 20 ms period			2 5 5 5 0.5		A V V W
T <sub>stg</sub> T <sub>j</sub>	Storage temperature Operating junction temperature		-40 -		150 125		သံံ

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/ $\mu$ s.

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## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-mb</sub>	Thermal resistance		-	-	1.3	K/W
R <sub>th j-a</sub>	junction to mounting base Thermal resistance junction to ambient	minimum footprint, FR4 board	-	55	-	K/W

## STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

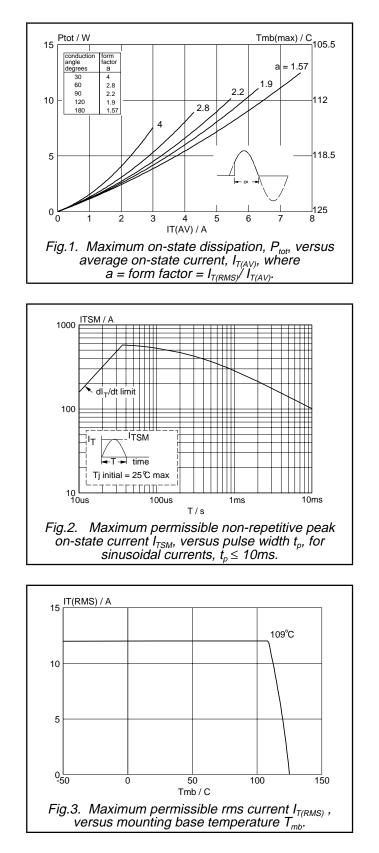
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>GT</sub>	Gate trigger current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	2	15	mA
	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	10	40	mA
	Holding current	$V_{\rm D} = 12 \text{ V}; \text{ I}_{\rm GT} = 0.1 \text{ A}$	-	7	20	mA
Ϋ <sub>τ</sub>	On-state voltage	$I_{T} = 23 \text{ A}$	-	1.4	1.75	V
V <sub>GT</sub>	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; \text{ I}_{\rm T} = 0.1 \text{ A}$	-	0.6	1.5	V
-		$V_{D} = V_{DRM(max)}$ ; $I_{T} = 0.1 \text{ A}$ ; $T_{j} = 125 \text{ °C}$	0.25	0.4	-	V
I <sub>D</sub> , I <sub>R</sub>	Off-state leakage current	$V_D^{D} = V_{DRM(max)}^{DRM(max)}; V_R = V_{RRM(max)}; T_j = 125 \text{°C}$	-	0.1	0.5	mA

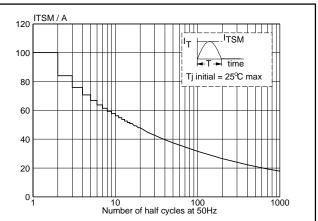
## **DYNAMIC CHARACTERISTICS**

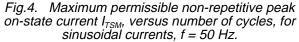
 $T_j = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV <sub>D</sub> /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$ exponential waveform;				
		Gate open circuit	50	130	-	V/µs
		$R_{GK} = 100 \Omega$	200	1000	-	V/µs
t <sub>gt</sub>	Gate controlled turn-on time	$I_{TM} = 40 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A}; dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	-	μs
t <sub>q</sub>	Circuit commutated turn-off time	$V_{D} = 67\% V_{DRM(max)}; T_{j} = 125 °C;$ $I_{TM} = 20 A; V_{R} = 25 V; dI_{TM}/dt = 30 A/\mu s;$ $dV_{D}/dt = 50 V/\mu s; R_{GK} = 100 \Omega$	-	70	-	μs

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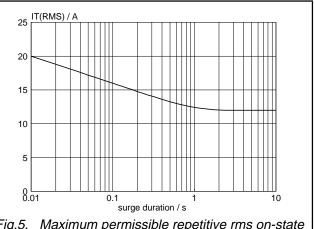
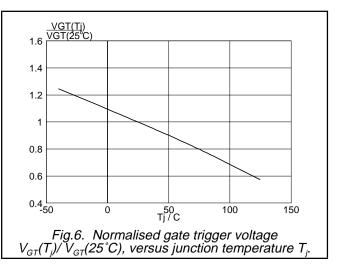


Fig.5. Maximum permissible repetitive rms on-state current  $I_{T(RMS)}$ , versus surge duration, for sinusoidal currents, f = 50 Hz;  $T_{mb} \le 109$ °C.



# **BT151B** series

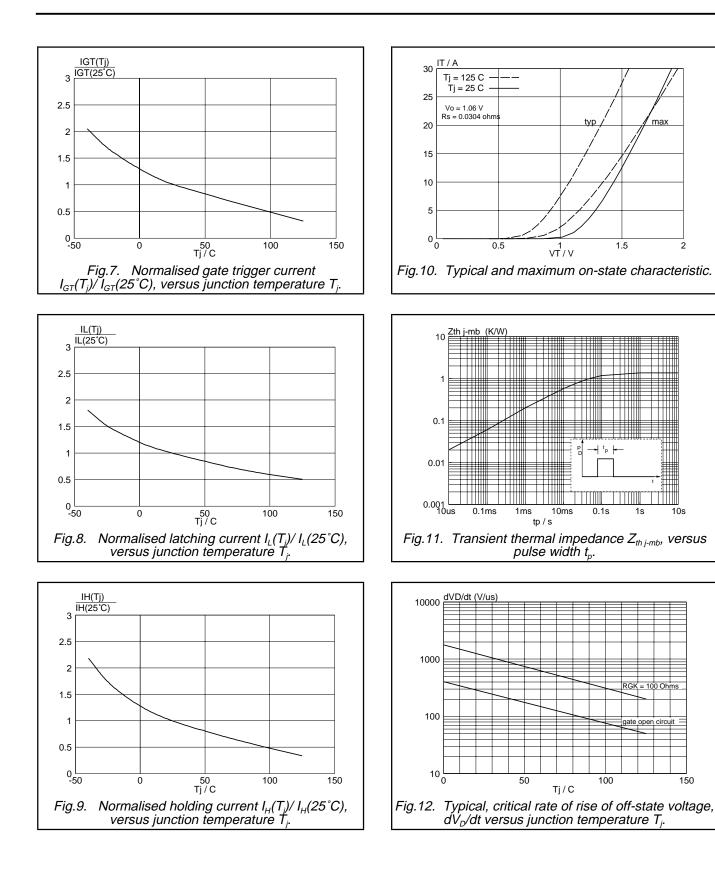
max

2

1s

ate open circuit

10s

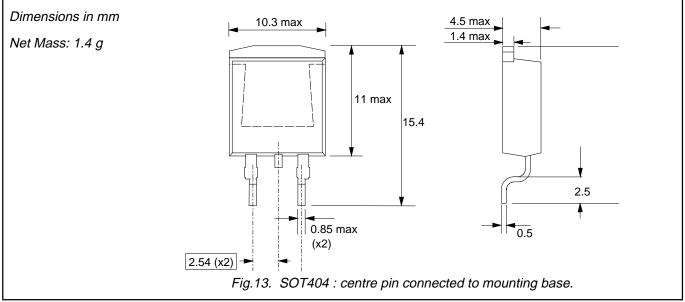


150

#### Product specification

## BT151B series

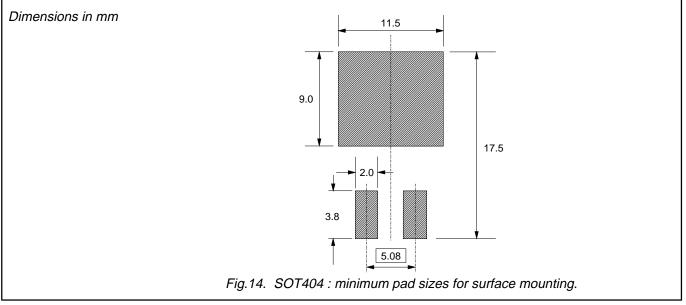
#### **MECHANICAL DATA**



#### Notes

1. Epoxy meets UL94 V0 at 1/8".

#### **MOUNTING INSTRUCTIONS**



#### Notes

1. Plastic meets UL94 V0 at 1/8".

## DEFINITIONS

Data sheet status					
Objective specification This data sheet contains target or goal specifications for product development.					
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.				
Product specification	Product specification This data sheet contains final product specifications.				
Limiting values					
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.					
Application information					
Where application information is given, it is advisory and does not form part of the specification.					
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