BUT11APX

GENERAL DESCRIPTION

Enhanced performance, new generation, high-voltage, high-speed switching npn transistor in a plastic full-pack envelope intended for use in horizontal deflection circuits of colour television receivers. Features exceptional tolerance to base drive and collector current load variations resulting in a very low worst case dissipation.

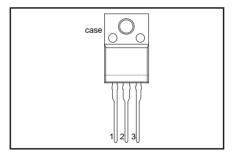
QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CESM}	Collector-emitter voltage peak value	$V_{RF} = 0 \text{ V}$	-	1000	V
V _{CBO}	Collector-Base voltage (open emitter)		-	1000	V
V _{CEO}	Collector-emitter voltage (open base)		-	450	V
I _C	Collector current (DC)		-	5	Α
I _{CM}	Collector current peak value		-	10	Α
P _{tot} V _{CEsat}	Total power dissipation	$T_{hs} \le 25 ^{\circ}C$	-	32	W
V _{CEsat}	Collector-emitter saturation voltage		-	1.5	V
Csat	Collector saturation current		3.5	-	Α
t _f	Fall time	I_{Csat} =2.5A, I_{B1} =0.5A, I_{B2} =0.8A	145	160	ns

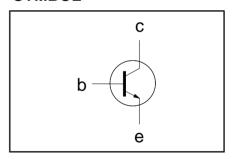
PINNING - SOT186A

PIN	DESCRIPTION	
1	base	
2	collector	
3	emitter	
case	isolated	

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum Rating System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CESM}	Collector to emitter voltage	$V_{BE} = 0 \text{ V}$	-	1000	V
V _{CEO}	Collector to emitter voltage (open base)		-	450	V
V _{CBO}	Collector to base voltage (open emitter)		-	1000	V
I _C	Collector current (DC)		-	5	Α
I I _{CM}	Collector current peak value		-	10	Α
I _B	Base current (DC)		-	2	Α
I _{BM}	Base current peak value		-	4	Α
P _{tot}	Total power dissipation	T _{hs} ≤ 25 °C	-	32	W
T _{stq}	Storage temperature		-65	150	°C
T _j	Junction temperature		-	150	°C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
R _{th j-hs}	Junction to heatsink	with heatsink compound	-	3.95	K/W
R _{th j-a}	Junction to ambient	in free air	55	-	K/W

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ISOLATION LIMITING VALUE & CHARACTERISTIC

 T_{hs} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65%; clean and dustfree	-		2500	V
C _{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

STATIC CHARACTERISTICS

T_{hs} = 25 °C unless otherwise specified

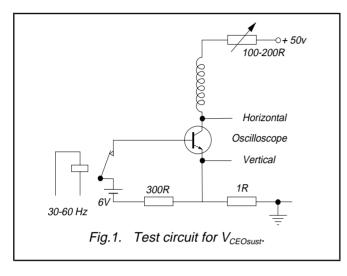
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CES}	Collector cut-off current ¹	$V_{BE} = 0 V; V_{CE} = V_{CESMmax}$	-	-	1.0	mA
I _{CES}		$V_{BE} = 0 \text{ V}; V_{CE} = V_{CESMmax};$ $T_i = 125 \text{ °C}$	-	-	2.0	mA
I _{EBO}	Emitter cut-off current	$V_{EB} = 9 \text{ V}; I_{C} = 0 \text{ A}$	-	-	10	mA
V _{CEOsust}	Collector-emitter sustaining voltage	$I_B = 0 \text{ A}; I_C = 100 \text{ mA};$ L = 25 mH	450	-	-	V
V_{CEsat}	Collector-emitter saturation voltages	$I_C = 3.0 \text{ A}; I_B = 0.6 \text{ A}$	-	0.25	1.5	V
V _{BEsat}	Base-emitter saturation voltage DC current gain	$I_{C} = 2.5 \text{ A}; I_{B} = 0.33 \text{ A}$ $I_{C} = 5 \text{ mA}; V_{CE} = 5 \text{ V}$	- 10	- 22	1.3 35	V
n _{FE} h _{FE}	DC current gain	$I_{\rm C} = 5 \text{mA}, \ V_{\rm CE} = 5 \text{V}$ $I_{\rm C} = 500 \text{mA}; \ V_{\rm CE} = 5 \text{V}$	14	25 25	35	
h _{FEsat}		$I_{\rm C} = 2.5 \text{ A}; V_{\rm CE} = 5 \text{ V}$	10	13.5	17	
h _{FEsat}		$I_{\rm C} = 3.5 \text{ A}; V_{\rm CE} = 5 \text{ V}$	8	10	12	

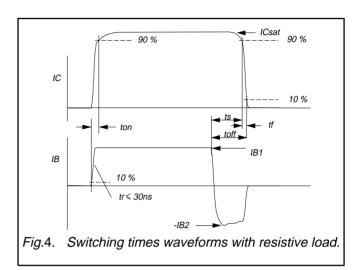
DYNAMIC CHARACTERISTICS

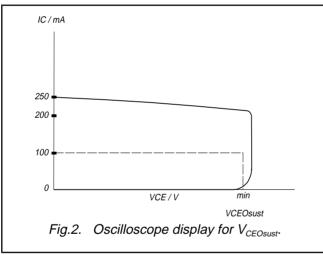
 T_{hs} = 25 °C unless otherwise specified

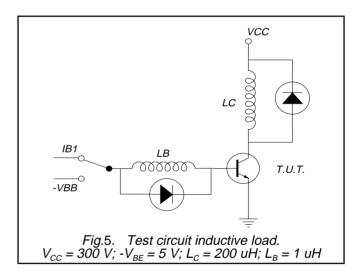
SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
	Switching times (resistive load)	$I_{Csat} = 2.5 \text{ A}; I_{B1} = -I_{B2} = 0.5 \text{ A}; R_1 = 75 \text{ ohms}; V_{BR2} = 4 \text{ V};$			
t _{on}	Turn-on time	, 552	0.5	0.7	μs
ا t _s ا t،	Turn-off storage time Turn-off fall time		3.3 0.33	4 0.45	μs us
1		$I_{Csat} = 2.5 \text{ A}; I_{B1} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}$,,,,
t _s t _f	Turn-off storage time Turn-off fall time	DD -	1.4 145	1.6 160	μs ns
	Switching times (inductive load)	$I_{Csat} = 2.5 \text{ A}; I_{B1} = 0.5 \text{ A}; L_{B} = 1 \mu\text{H}; -V_{BB} = 5 \text{ V}; T_{i} = 100 ^{\circ}\text{C}$			
t _s t _f	Turn-off storage time Turn-off fall time	DD - , , , - 3	1.7 160	1.9 200	μs ns

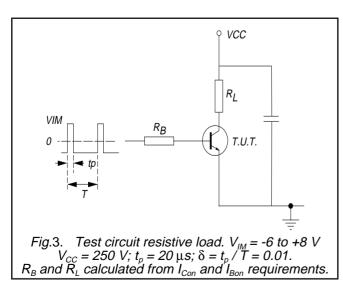
¹ Measured with half sine-wave voltage (curve tracer).

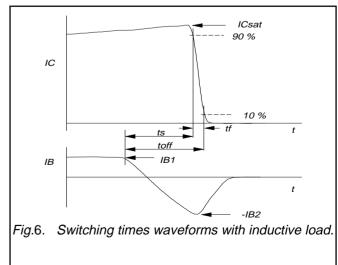






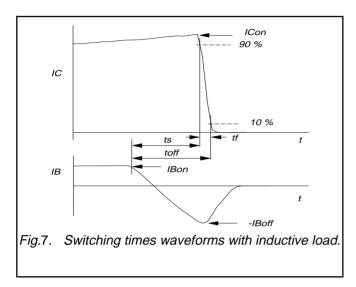






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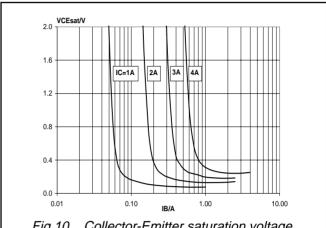
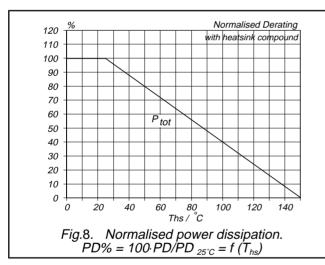


Fig.10. Collector-Emitter saturation voltage. Solid lines = typ values, $V_{CEsat} = f(IB)$; $T_j=25^{\circ}C$.



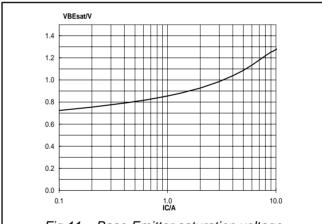
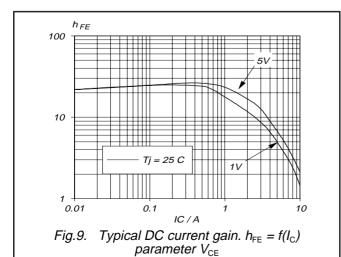
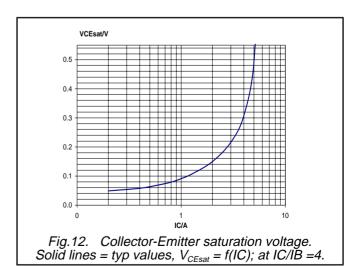
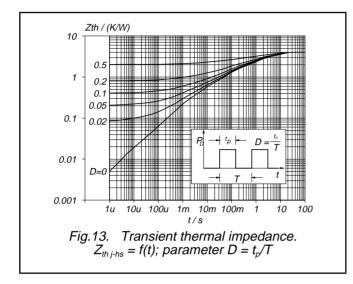
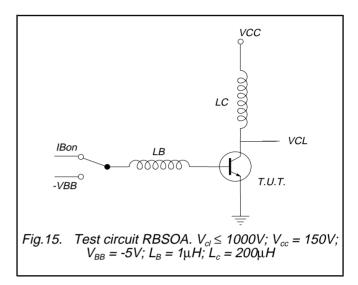


Fig.11. Base-Emitter saturation voltage. Solid lines = typ values, $V_{BEsat} = f(IC)$; at IC/IB = 4.









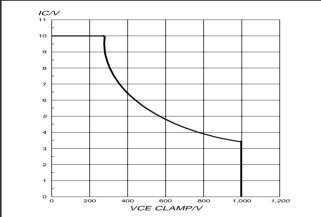


Fig.14. Reverse bias safe operating area. $T_j \le T_{j \max}$

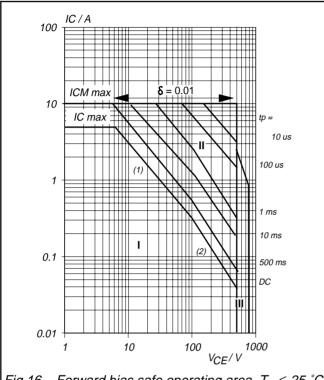


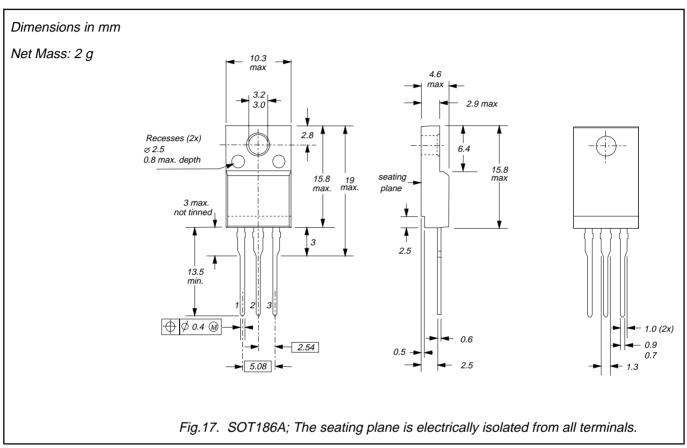
Fig.16. Forward bias safe operating area. $T_{hs} \le 25 \, ^{\circ}\text{C}$

- (1) (2) I

- III
- P_{tot} max and P_{tot} peak max lines. Second breakdown limits. Region of permissible DC operation. Extension for repetitive pulse operation. Extension during turn-on in single transistor converters provided that $R_{BE} \leq 100~\Omega$ and $t_p \leq 0.6~\mu$ s. Mounted with heatsink compound and 30 ± 5 newton force on the centre of the envelope. NB:
 - envelope.

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MECHANICAL DATA



- Notes
 1. Refer to mounting instructions for F-pack envelopes.
 2. Epoxy meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limitin or conference				

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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