

HFA12PA120C

Ultrafast, Soft Recovery Diode

Features

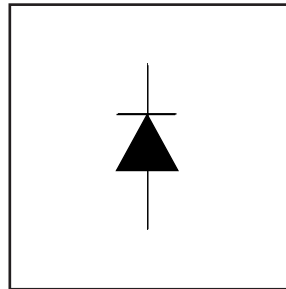
- Ultrafast Recovery
- Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- Guaranteed Avalanche
- Specified at Operating Conditions

Benefits

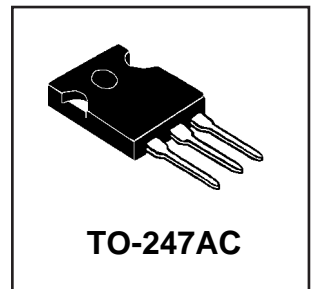
- Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- Higher Frequency Operation
- Reduced Snubbing
- Reduced Parts Count

Description

International Rectifier's HFA12PA120C is a state of the art center tap ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which results in performance which is unsurpassed by any rectifier previously available. The HFA12PA120C has basic ratings of 1200 volts and 6 amps per leg continuous current. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA12PA120C is ideally suited for applications in power supplies and power conversion systems (such as inverters, converters, UPS systems, and power factor correction circuits), motor drives, and many other similar applications where high speed, high efficiency is needed.



$V_R = 1200V$
$V_F(\text{typ.})^* = 2.4V$
$I_{F(AV)} = 6A$
$Q_{rr}(\text{typ.}) = 116nC$
$I_{RRM}(\text{typ.}) = 4.4A$
$t_{rr}(\text{typ.}) = 26ns$
$di_{(rec)M}/dt(\text{typ.})^* = 100A/\mu s$



Absolute Maximum Ratings

	Parameter	Max.	Units
V_R	Cathode-to-Anode Voltage	1200	V
$I_F @ T_C = 25^\circ C$	Continuous Forward Current		A
$I_F @ T_C = 100^\circ C$	Continuous Forward Current	6.0	
I_{FSM}	Single Pulse Forward Current	80	
I_{FRM}	Maximum Repetitive Forward Current	24	
$I_{AS} \textcircled{1}$	Maximum Single Pulse Avalanche Current	6.0	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	62.5	W
$P_D @ T_C = 100^\circ C$	Maximum Power Dissipation	25	
T_J	Operating Junction and	-55 to +150	$^\circ C$
T_{STG}	Storage Temperature Range		

* 125°C

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{BR}	Cathode Anode Breakdown Voltage	1200	—	—	V	I _R = 100μA
V _{FM}	Max Forward Voltage	—	2.7	3.0	V	I _F = 6A
		—	3.5	3.9		I _F = 12A
		—	2.4	2.8		I _F = 6A, T _J = 125°C
I _{RM}	Max Reverse Leakage Current	—	0.26	5.0	μA	V _R = V _R Rated
		—	110	500		T _J = 125°C, V _R = 0.8 x V _R Rated
C _T	Junction Capacitance	—	9.0	14	pF	V _R = 200V
L _S	Series Inductance	—	12	—	nH	Measured lead to lead 5mm from package body

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t _{rr}	Reverse Recovery Time	—	26	—	ns	I _F = 1.0A, di _F /dt = 200A/μs, V _R = 30V
t _{rr1}	See Fig. 5, 10	—	53	80		T _J = 25°C
t _{rr2}		—	87	130	A	T _J = 125°C
I _{RRM1}	Peak Recovery Current	—	4.4	8.0		T _J = 25°C
I _{RRM2}	See Fig. 6	—	5.0	9.0	nC	T _J = 125°C
Q _{rr1}	Reverse Recovery Charge	—	116	320		T _J = 25°C
Q _{rr2}	See Fig. 7	—	233	585	A/μs	T _J = 125°C
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current	—	180	—		T _J = 25°C
di _{(rec)M} /dt2	During t _b See Fig. 8	—	100	—	T _J = 125°C	

Thermal - Mechanical Characteristics

	Parameter	Min.	Typ.	Max.	Units
T _{lead} ②	Lead Temperature	—	—	300	°C
R _{θJC}	Thermal Resistance, Junction to Case	—	—	2.0	K/W
		—	—	1.0	
		—	—	40	
R _{θJA} ③	Thermal Resistance, Junction to Ambient	—	—	40	g
R _{θCS} ④	Thermal Resistance, Case to Heat Sink	—	0.25	—	
Wt	Weight	—	6.0	—	(oz)
		—	0.21	—	
	Mounting Torque	6.0	—	12	Kg-cm
		5.0	—	10	lbf·in

- ① L=100μH, duty cycle limited by max T_J
- ② 0.063 in. from Case (1.6mm) for 10 sec
- ③ Typical Socket Mount
- ④ Mounting Surface, Flat, Smooth and Greased

Instantaneous Forward Current - I_F (A)

Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

Reverse Current - I_R (μ A)

Fig. 2 - Typical Reverse Current vs. Reverse Voltage

Junction Capacitance - C_T (pF)

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

Fig. 4 - Maximum Thermal Impedance Z_{thjc} Characteristics

t_{rr} - (nC)

I_{rr} - (A)

Fig. 5 - Typical Reverse Recovery vs. di_f/dt ,
(per Leg)

Fig. 6 - Typical Recovery Current vs. di_f/dt ,
(per Leg)

Q_{rr} - (nC)

$di_{(rec)M}/dt$ - (A / μ s)

Fig. 7 - Typical Stored Charge vs. di_f/dt ,
(per Leg)

Fig. 8 - Typical $di_{(rec)M}/dt$ vs. di_f/dt ,
(per Leg)

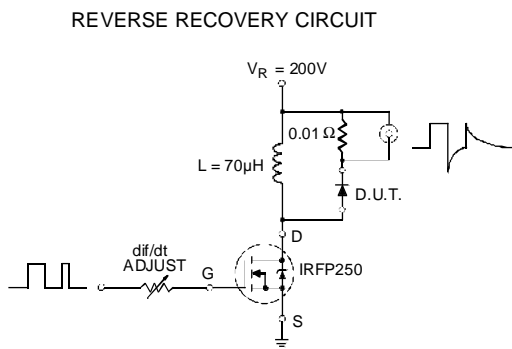


Fig. 9 - Reverse Recovery Parameter Test Circuit

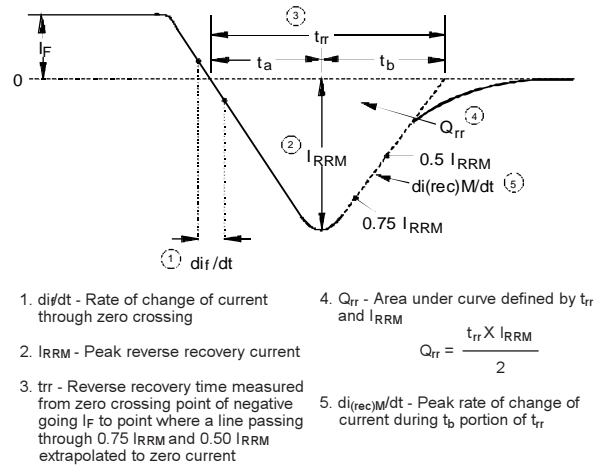


Fig. 10 - Reverse Recovery Waveform and Definitions

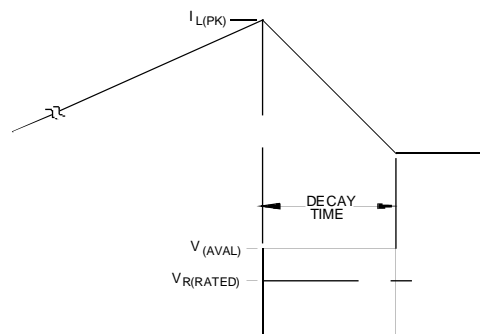
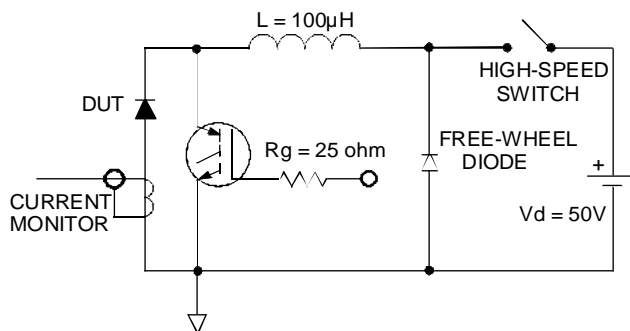
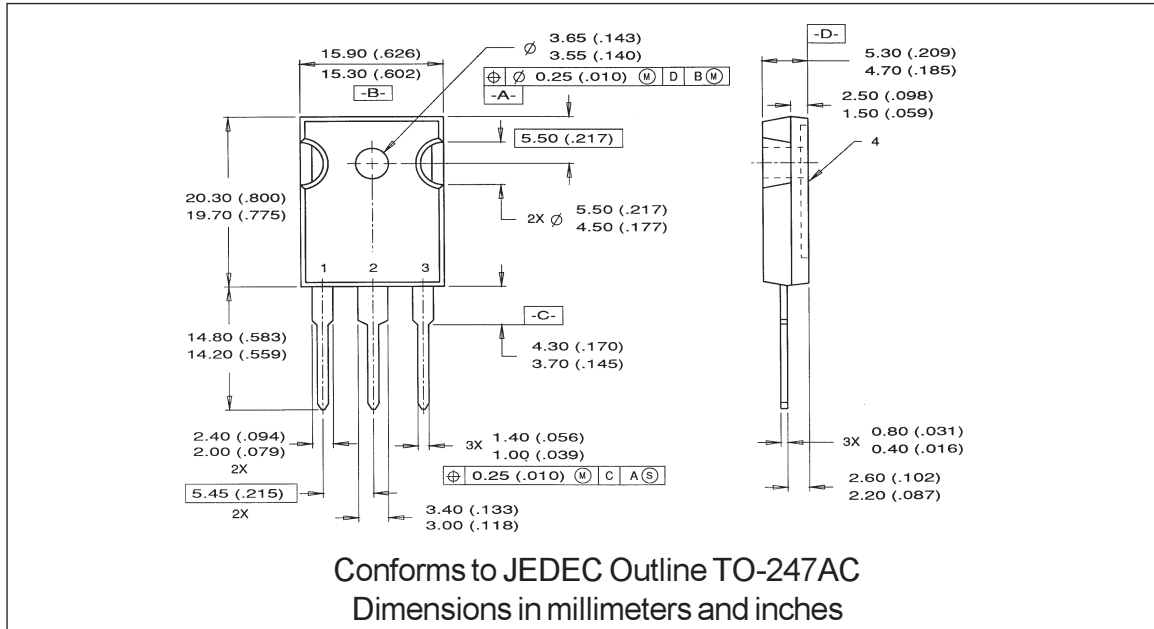


Fig. 11 - Avalanche Test Circuit and Waveforms

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Data and specifications subject to change without notice.

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